

## Octet

#1	#2	#3	#4
Target logical address (0x20)	PID (0x01)	Instruction (0x64)	Key (0x00)
Initiator logical address (SpW#1 : 0x40 SpW#2 : 0x50)	Transaction ID (MS)	Transaction ID (LS)	Extended address (0x00)
Address (MS) (0x00)	Address (0x00)	Address (0x00)	Address (LS) (0x00)
Data length (MS) (0x00)	Data length (0x00)	Data length (LS) (0x00)	Header CRC
Frame number (MS)	Frame number	Frame number	Frame number (LS)
Flag (MS) (0xAA)	Flag (0x55)	Flag (0xAA)	Flag (LS) (0x55)
Data1 ADC1 (MS)	Data1 ADC1 (LS)	Data1 ADC2 (MS)	Data1 ADC2 (LS)
Data2 ADC1 (MS)	Data2 ADC1 (LS)	Data2 ADC2 (MS)	Data2 ADC2 (LS)
...	...	...	...
Padding (0xCE)	Padding (0xAC)	Padding (0xEA)	Data CRC
EOP			